Fig. 1

High-tech equipment is needed for testing of our ultra-fast ADC's.

Fig. 2

Hardware inspection of assembled prototype boards.
Infra-red image for assessment of the operating temperature of the assembled components.

Performing analysis of prototype hardware, and in-computer simulation of signal processing based on collected data.

The program aims to achieve ultra-fast A/D conversion using hybrid architectures involving both photonic and electronic circuits. A/D conversion at such high speeds require considerable efforts in ensuring signal and data integrity, proper signal response over a very wide bandwidth and a backbone supporting massive high-speed data transfer and digital processing.